Claim 16, line 1, "the branch" has been changed to "branch".

Claim 17, lines 5-6, "the channel states for isolated os and is "i.e." has been changed to "channel states for isolated 0s and 1s, i.e.".

Claim 19, lines 12, 14 and 15, "a predetermined threshold" has been changed to "the predetermined threshold"; and line 13, "below;" has been changed to "below".

Claim 21, line 24, "a symbol" has been changed to "the symbol".

Claim 22, line 2, "states" has been changed to "state"; and line 3, "the branch" has been changed to "branch".

Claim 23, line 6, ""i.e." and "..00100" have been changed to "i.e." and "...00100", respectively.

Claim 24, line 3, ") the" has been changed to "), the".

Claim 25, line 1, "(Original)" has been changed to "(Currently amended)".

Claim 26, line 6, "the bit error rate" has been changed to "a bit error rate".

3.   The following changes to the drawings have been approved by the examiner and agreed upon by applicant: the replacement sheet of Figure 1 filed on November 24, 2009 has been accepted by the examiner. However, Figure 17 is required to label "prior art" as described in paragraph [0070] of the specification and objected in the last Office Action mailed on June 25, 2009, where "Figure 17" is mistyped as "Figure 2". In order to avoid abandonment of the application, applicant must make these above agreed upon drawing changes.